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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/831,315	05/08/2001	Keizaburo Kuramasu	OGOH:077	2858
7	590 06/07/2004		EXAMINER	
Parkhurst & Wendel Suite 210			ERDEM, FAZLI	
1421 Prince Street			ART UNIT	PAPER NUMBER
Alexandria, VA 22314-2805			2826	
			DATE MAIL ED: 06/07/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

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•		Application No.	Applicant(s)		
Office Action Commence		09/831,315	KURAMASU, KEIZABURO		
	Office Action Summary	Examin r	Art Unit		
		Fazli Erdem	2826		
Period fo	The MAILING DATE of this communication a or Reply	pp ars on the cover sheet with the o	correspondence address		
A SH THE - Exte after - If the - If NO - Faild Any	IORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION ensions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. The period for reply specified above is less than thirty (30) days, a reproved for reply is specified above, the maximum statutory period reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mail the patent term adjustment. See 37 CFR 1.704(b).	I. 1.136(a). In no event, however, may a reply be tireply within the statutory minimum of thirty (30) day d will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. (35 U.S.C. § 133).		
Status			•		
1)⊠	Responsive to communication(s) filed on <u>06</u>	November 2003.			
· —	is action is FINAL . 2b) This action is non-final.				
3)[
Disposit	ion of Claims				
5)□ 6)⊠	Claim(s) <u>1-38</u> is/are pending in the application 4a) Of the above claim(s) <u>29-38</u> is/are withdray Claim(s) is/are allowed. Claim(s) <u>1,14-17 and 20-23</u> is/are rejected. Claim(s) <u>2-13,18,19 and 24-28</u> is/are objected. Claim(s) are subject to restriction and	awn from consideration.			
Applicat	ion Papers				
	The specification is objected to by the Examin				
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11)	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.				
Priority (under 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
		·			
Attachmen	ıt(s)				
1) Notic	ce of References Cited (PTO-892)	4) Interview Summary	(PTO-413)		
2) Notic	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail D	ate		
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/06 er No(s)/Mail Date <u>11/01/2001</u> .	8) 5) Notice of Informal F 6) Other:	Patent Application (PTO-152)		

DETAILED ACTION

Allowable Subject Matter

1. Claims 2-13, 18, 19, and 24-28 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1, 14 and 15 rejected under 35 U.S.C. 103(a) as being unpatentable over Misawa et al. (5,250,931) in view of Onisawa et al. (6,226,060).

Regarding Claims 1, 14 and 15, Misawa et al. disclose an active matrix panel having display and driver TFTs on the same substrate where an active matrix panel including a matrix of driving electrodes couples through thin film transistor switches to a corresponding source line and gate line and at least one of a driver circuit including complementary thin film transistors for driving the source and/or gate lines of the picture elements on the substrate. The thin film transistors of the active matrix have the same cross-sectional structure as the thin film transistors forming the driver circuit and are formed during the same patterning process. Misawa et al. fail to disclose the required via-hole structure that connects the driver circuits to the external circuit. However, Onisawa et al. disclose an active matrix type liquid crystal display device having

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chromium alloy connecting portions at pixel electrode or near driving circuit terminals where in Claim 1, the required connection structure is disclosed.

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It would have been obvious to one of having ordinary skill in the art at the time the invention was made to include the required connection structure in Misawa et al. as taught by Onisawa et al. in order to have a semiconductor display device with better reliability.

3. Claims 16, 17 and 20-23 rejected under 35 U.S.C. 103(a) as being unpatentable over Misawa et al. (5,250,931) Yamagishi (5,365,091) further in view of Tanizawa (4,928,164).

Regarding Claims 16, 17 and 20-23, Misawa et al. disclose an active matrix panel having display and driver TFTs on the same substrate where an active matrix panel including a matrix of driving electrodes couples through thin film transistor switches to a corresponding source line and gate line and at least one of a driver circuit including complementary thin film transistors for driving the source and/or gate lines of the picture elements on the substrate. The thin film transistors of the active matrix have the same cross-sectional structure as the thin film transistors forming the driver circuit and are formed during the same patterning process. Misawa et al. fail to disclose the required power/bus line structure around the periphery and the required power/bus groove/covering structure. However, Yamagishi discloses a semiconductor integrated circuit device where in the abstract section the power/bus line structure around the periphery is disclosed. Furthermore, Tanizawa discloses an integrated device having a chip where in abstract section the required groove/covering structure is disclosed.

It would have been obvious to one of having ordinary skill in the art at the time the invention was made to include the required power/bus line and the required groove/covering

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structure in Misawa et al. as taught by Yamagishi and Tanizawa respectively in order to have a

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liquid crystal display device with higher reliability.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Fazli Erdem whose telephone number is (571) 272-1914. The

examiner can normally be reached on M - F 8:00 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

FE

May 11, 2004

WATHAN J. FLYNN

RVISORY PATENT EXAMINER

HNOLOGY CENTER 2800